Polyhedral Transformations of Explicitly Parallel Programs

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Explicit Parallelism and Motivatic

- **3** Our Approach
- Preliminary Results
- 5 Related Work
- 6 Conclusions, Future work and Acknowledgments

Introduction

- Software with explicit parallelism is on rise
- Two major compiler approaches for program optimizations
 - AST-based
 - Polyhedral-based
- Past work on transformations of parallel programs using AST-based approaches
 - E.g., [Nicolau et.al 2009], [Nandivada et.al 2013]
- Polyhedral frameworks for analysis and transformations of explicitly parallel programs ??

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Introduction

- Explicit parallelism is different from sequential execution
 - Partial order instead of total order
 - $\bullet\,$ No execution order among parallel portions $\rightarrow\,$ no dependence
- For the compiler, explicit parallelism can mitigate imprecision that accompanies unanalyzable data accesses from a variety of sources.

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- Unrestricted pointer aliasing
- Unknown function calls
- Non-affine constructs
 - Non-affine expressions in array subscripts
 - Indirect array subscripts
 - Non-affine loop bounds
 - Use of Structs

2 Explicit Parallelism and Motivation

- **3** Our Approach
- Preliminary Results
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Explicit Parallelism

- Logical parallelism is a specification of a **partial order**, referred to as a **happens-before** relation
 - HB(S1, S2) = true \leftrightarrow S1 must happen before S2
- Currently, we focus on explicitly parallel programs that satisfy serial-elision property

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- Doall parallelism
- Doacross parallelism

Explicit Parallelism - Doall (OpenMP)

- In case of OpenMP, **Doall** parallelism is equivalent to the **parallel for** clause.
- Happens-before relations exist only among statements in the same iteration
 - Guarantees no cross-iteration dependence

1	#pragma	omp parallel	for
2	for	(i-loop) {	
3		S1;	
4		S2;	
5		S3;	
6	}		



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Explicit Parallelism and Motivation

Explicit Parallelism - Doall (OpenMP) - Example

• LU Decomposition - Rodinia benchmarks [Shuai et.al 09]

- j,k-loops are annotated as parallel loops and k-loop is parallel with a reduction on array a
- Poor spatial locality because of access pattern k*size+j for array a
- With happens-before relations from **doall**, loop permutation can be applied to improve spatial locality.

xplicit Parallelism and Motivation

Explicit Parallelism - Doall (OpenMP) - Example

Permuted kernel

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```
for (i = 0; i < size; i++) {
    #pragma omp parallel for reduction(+:a) private(j)
    for (k = 0; k < i; k++) {
        for (j = i; j < size; j++) {
            a[i*size+j] -= a[i*size+k] * a[k*size+j];
        }
    }
    ....
}</pre>
```

- 1.25X performance on Intel Xeon Phi coprocessor with 228 threads and input size as 2K
- Array subscripts are non-affine (but can be made affine with delinearization and perform permutation) [Tobias et.al 15]

Explicit Parallelism - Doacross (OpenMP)

- In case of OpenMP, **Doacross** parallelism is equivalent to proposed extension [Shirako et.al 13] to the **ordered** clause (appears in OpenMP 4.1).
- To specify cross-iteration dependences of a parallelized loop

```
1 #pragma omp parallel for ordered(1)
2 for (i-loop) {
3 S1;
4 #pragma omp ordered depend(sink: i-1)
5 S2;
6 #pragma omp ordered depend(source: i)
7 S3;
8 }
```



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Explicit Parallelism - Doacross (OpenMP) - Example

```
1 // Assume array A is a nested array
 2 #pragma omp parallel for ordered(3)
 3 for (t = 0; t \le PB_TSTEPS - 1; t++) {
    for (i = 1; i \le PB N - 2; i++) {
       for (i = 1; i \le PB N - 2; i++)
 6 \# pragma omp ordered depend(sink: t, i-1, j+1) depend(sink: t, i, j-1) \setminus
 7
        depend(sink: t-1, i+1, j+1)
 8
         A[i][j] = (A[i-1][j-1] + A[i-1][j] + A[i-1][j+1] + A[i][j-1]
 9
                  + A[i][j] + A[i][j+1] + A[i+1][j-1] + A[i+1][j]
                  + A[i+1][j+1]) / 9.0;
11 #pragma omp ordered depend(source: t.i.i)
12
13
     }
14 }
```

- 2-dimensional 9 point Gauss Seidel computation [PolyBench]
- Annotated as 3-D Doacross loop nest
- Even though loop nest has affine accesses, C's unrestricted aliasing semantics for nested arrays can prevent a sound compiler analysis from detecting exact cross iteration dependences.

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Explicit Parallelism - Doacross (OpenMP) - Example

```
1 // Assume array A is a nested array
 2 #pragma omp parallel for ordered(3)
 3 for (t = 0; t \le PB_TSTEPS - 1; t++) {
    for (i = 1; i \le PB_N - 2; i++)
       for (j = 1; j \le PB_N - 2; j++) {
 6 #pragma omp ordered depend(sink: t, i-1, j+1) depend(sink: t, i, j-1)
       depend(sink: t-1, i+1, j+1)
7
        A[i][j] = (A[i-1][j-1] + A[i-1][j] + A[i-1][j+1] + A[i][j-1]
8
 9
                 + A[i][j] + A[i][j+1] + A[i+1][j-1] + A[i+1][j]
                 + A[i+1][i+1]) / 9.0;
11 #pragma omp ordered depend(source: t.i.i)
12
13
     }
14 }
```

- Through cross-iteration dependences via doacross, loop skewing and tiling can be performed to improve both locality and parallelism granularity.
- 2.2X performance on Intel Xeon Phi coprocessor with 228 threads and input for 100 time steps on a 2K X 2K matrix.

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Approach - Idea

- Overestimate dependences based on the sequential order
 - ignore parallel constructs
- Improve dependence accuracy via explicit parallelism
 - obtain happens-before relations from parallel constructs
 - intersect HB relations with conservative dependences
- Transformations via polyhedral optimizers
 - PLuTo [Bondhugula et.al 2008]
 - Poly+AST [Shirako et.al 2014]
- Code generation with parallel constructs
- Focus on
 - Doall and Doacross constructs
 - Non-affine subscripts and Indirect arrays subscripts

Algorithm - Framework



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Algorithm - Motivation

- Conservative dependence analysis
 - May-information on access range of non-affine array subscripts
- Our existing implementation uses scoplib format for convenience (rather than openscop)
 - No support for access relations in scoplib format (to the best of our knowledge)

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- What could potentially represent possible access range of non-affine subscript in polyhedral model?
 - Iterator ?
 - Cannot be part of loops
 - Parameter ?
 - Cannot be loop invariant

Approach - Dummy vector

- Approach use dummy variables to overestimate access range of non-affine subscripts
 - A dummy corresponds to a non-affine expression
 - Compute conservative dependences via dummy variables
- Dummy vector = vector of dummy variables from same scop
- Each dynamic instance of a statement S is uniquely identified by combination of:

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- its iteration vector $(\vec{i_S})$
- dummy vector $(\vec{d_S})$
- parameter vector (\vec{p})

```
Our Approach
```

Approach - Dummy vector - Example

```
1 int A[N][N], x[N][N], y[N][N];
2 #pragma omp parallel for
3 for (i = 0; i < N; i++)
4 for (j = 0; j < N; c++)
5 A[j][i] = A[x[j][i]] [y[j][i]];
```

- Non-affine: Two indirect array subscripts (x[j][i], y[j][i])
- Replace non-affine constructs with dummy variables
- Iteration vector $(\vec{i_S}) = (i, j)$, Parameter vector $(\vec{p}) = (N)$

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• Dummy vector $(\vec{d_S}) = (dmy_1, dmy_2) = (x[j][i], y[j][i])$

Algorithm - Conservative Analysis

- Replace non-affine expressions in array subscripts with dummy variables as part of pre-processing
- Create affine inequalities for dummy variables based on array declarations and incorporate them into iteration domain
- In case of indirect array subscripts, also associate the index arrays into **read array** list
- Forward the SCoP to CANDL (dependence analyzer)

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Algorithm - Conservative Analysis - Example

```
1 int A[N][N], x[N][N], y[N][N];
2 #pragma omp parallel for
3 for (i = 0; i < N; i++)
4 for (j = 0; j < N; c++)
5 A[j][i] = A[dmy1][dmy2]; // S
```

$$\begin{array}{ll} \mathcal{P}_{1}^{S \to S}(Depth=1) & \mathcal{P}_{2}^{S \to S}(Depth=2) \\ i \leq i' - 1 & i = i', j \leq j' - 1 \\ j = dmy_{1}, i = dmy_{2} & j = dmy_{1}, i = dmy_{2} \\ 0 \leq i, j, i', j' \leq (N-1) & 0 \leq i, j, i', j' \leq (N-1) \\ 0 \leq dmy_{1}, dmy_{2} \leq (N-1) & 0 \leq dmy_{1}, dmy_{2} \leq (N-1) \\ 0 \leq dmy_{1}', dmy_{2}' \leq (N-1) & 0 \leq dmy_{1}', dmy_{2}' \leq (N-1) \end{array}$$

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Algorithm - Conservative Analysis - Elimination

After computation of conservative dependences from CANDL, we eliminate dummy variables using Fourier-Motzkin elimination from

- Conservative dependences
- Iteration domain

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Algorithm - Conservative Analysis - Elimination - Example



Source vector: (i, j, N) Sink vector: (i', j', N)

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Algorithm - Reflection of happens-before relations

- Let C_d denote happens-before relations on loop at depth = d
 - C_d : constraint under which a dependence can exist
- If there are no explicit parallel constructs on a loop, then sequential order would be happens-before relations on that loop
- Happens-before relations in the following program

```
C_{1}: i = i'
C_{2}: i = i', j = j' - 1
Source vector: (i, j, N) Sink vector: (i', j', N)
```

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Algorithm - Reflection of happens-before relations

- 1: Input: conservative dependences \mathcal{P}'_{a} and constraints \mathcal{C}
- 2: for each dependence polyhedron $\mathcal{P}_d^{S_i \to S_j}$ in \mathcal{P}' do
- 3: **for** each constraint $C_e^{S_k \to S_l}$ in C **do**
- 4: **if** $S_i = S_k \& S_j = S_l \& d = e$ **then** 5: $\mathcal{P}'_{a}^{(S_i \to S_j)} := \mathcal{P}'_{a}^{(S_i \to S_j)} \cap C_e^{S_k \to S_l};$
- 5: $\mathcal{P}'_{d}^{j,j,j,j} := \mathcal{P}'_{d}^{j,j,j,j}$
- 7: end for
- 8: Add the reflected polyhedron $\mathcal{P}''_{d}^{i'S_{i} \rightarrow S_{j}}$ to \mathcal{P}'' ;
- 9: end for
- 10: **Output:** dependence polyhedra after reflection \mathcal{P}''

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Algorithm - Reflection of happens-before relations -Example - (Depth = 1)



Source vector: (i, j, N) Sink vector: (i', j', N)

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Algorithm - Reflection of happens-before relations -Example - (Depth = 2)



Source vector: (i, j, N) Sink vector: (i', j', N)

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Algorithm - Code generation

• Transformed kernel after loop permutation

```
1 int A[N][N], x[N][N], y[N][N];
2 for (j = 0; j < N; i++)
3 #pragma omp parallel for
4 for (i = 0; i < N; c++)
5 A[j][i] = A[x[j][i]][y[j][i]]; // S
```

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Algorithm - Implementation

- Implementation is in-progress
- Completed modules:
 - AST Modifier, AST to SCoP converter, Elimination of dummy variables

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- Intersection with Happens-before relations
- AST to Target
- In-progress modules:
 - Integration with optimizers such as PLuTo, Poly+AST
 - Code generation for do-across

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Rodinia Benchmarks

- Studied 18 explicitly parallel OpenMP-C Rodinia benchmarks
- Identified non-affine constructs used in the benchmarks that limit the use of polyhedral frameworks
 - Indirect Array Subscript (IAS), Non-affine Array Subscript(NAS), Use of Structs (S), Functions (F)
- Potential opportunities for polyhedral loop transformations that can be enabled through our approach
 - Loop permutation, Fusion, Skewing, Tiling, Doacross parallelism, Vectorization

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Rodinia Benchmarks

Ennitations						
Kernel	NAS	IAS	S	F	Transformations	
b+ tree		\checkmark	\checkmark			
backprop				\checkmark	perm, fuse, vect	
bfs		\checkmark	\checkmark			
cfd	\checkmark			\checkmark		
heartwall				\checkmark		
hotspot	\checkmark				doacross, fuse, skew, tile, vect	
kmean				\checkmark	perm, fuse, vect	
lavaMD	\checkmark	\checkmark	\checkmark			
leukocyte				\checkmark	fuse, vect	

Limitations

Table: Limitations and possible transformations in Rodinia benchmarks (NAS: Non-affine Array Subscript, IAS: Indirect Array Subscript, S: Struct, F: Function, and perm/fuse/skew/tile/doacross/vect: loop permutation/fusion/skewing/tiling/doacross parallelism/vectorization)

Rodinia Benchmarks (Continued)

Limitations						
Kernel	NAS	IAS	S	F	Transformations	
lud	\checkmark				perm, vect	
mummergpu			\checkmark	\checkmark		
myocyte	\checkmark			\checkmark		
nn	\checkmark			\checkmark		
nw	\checkmark			\checkmark	doacross, skew, perm	
particle filter	\checkmark			\checkmark	fuse, vect	
path finder					doacross, skew, tile	
srad	\checkmark					
streamcluster	\checkmark	\checkmark	\checkmark	\checkmark		

Table: Limitations and possible transformations in Rodinia benchmarks (NAS: Non-affine Array Subscript, IAS: Indirect Array Subscript, S: Struct, F: Function, and perm/fuse/skew/tile/doacross/vect: loop permutation/fusion/skewing/tiling/doacross parallelism/vectorization)

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Preliminary results

$\mathsf{Speedup} = \frac{\mathsf{Exec time of optimized parallel code}}{\mathsf{Exec time of input parallel code}}$

Kernel	Benchmark	Best Speedup	Transformation
Backprop	Rodinia	28X	Permutation, Vect
Hotspot	Rodinia	2.25X	Skewing, Tiling, Doacross
Lud	Rodinia	1.15X	Permutation, Vect
Particlefilter	Rodinia	1.05X	Fusion

Table: Performance improvements on Intel Xeon Phi with 228 threads¹

¹Some steps (e.g., code gen) were done mañually? → (Ξ)→ (Ξ)→ (Ξ)→ (Ξ)→ (32/42) Prasanth Chatarasi, Jun Shirako, Vivek Sarkar MIPACT Workshop, 19 Jan 2015

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Related Work - Explicitly parallel programs

- Extension of array data-flow analysis to data-parallel and/or task-parallel languages [Collard et.al 96]
- Adaptation of array data-flow analysis to the X10 programs with finish/async parallelism [Yuki et.al 13]
- In these approaches, happens-before relations are first analyzed and data-flow is computed based on the partial order imposed by happens-before relations.
- Our approach first overestimates dependences based on the sequential order and intersect with the happens-before relations from explicitly parallel constructs.
- Our work focuses on transformation of explicitly parallel programs for improved performance where as above works are focused on analysis.

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Compile time Approaches for non-affine constructs

- Pugh et.al 91, Maslov et.al 94,
- Uses uninterpreted function symbols to represent non-affine constructs
- Generates dependence relations by approximating with affine dependence relations
- We prune conservative dependences using happens-before relations from explicit parallel constructs

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Run time Approaches for non-affine constructs

- Doerfert et.al 13, Simburger et.al 14,
- Speculative polyhedral optimization techniques, Auto tuning
- Modeling using semi-algebraic sets and real algebra (POLLY)
 - Worst case doubly exponential complexity
- Inspector/ Executor: Strout et.al 03, Basumallik et.al 06, Venkat et.al 14,
- Integration into Polyhedral compiler collection chain
- We perform analysis and transformations at compile time

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Related Work - PENCIL

- Platform Neutral Compute Intermediate Language
- Automatic parallelization on multi-threaded SIMD hardware for DSL's
- Provides extensions and directives that allow users to supply dependence information
- We are interested in leveraging happens-before relations from programs written in general purpose languages like OpenMP, X10, Habanero-C whereas PENCIL is focused on supporting DSL's in which certain coding rules are enforced related to pointer aliasing, recursion and unstructured control flow.

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- Introduced an approach that reflects **happens-before** relations from explicitly parallel constructs in the dependence polyhedra to mitigate conservative dependence analysis.
- Studied 18 explicitly-parallel OpenMP benchmarks from Rodinia suite.
- Shown that the use of explicit parallelism enables larger set of polyhedral transformations, compared to what might have been possible if the input program was sequential.

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Future work and Acknowledgments

• Future work

- Incorporate additional explicit parallel constructs such as barrier and task parallelism
- Additional transformations for explicit parallel programs
- Acknowledgments
 - Rice Habanero Extreme Scale Software Research Group

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• IMPACT 2015 chairs, reviewers and shepherd

Backup - Conservative Analysis

- Access relations [Wonnacott thesis] and uninterpreted function symbols [Omega library] could have been used instead of dummy variables, but our implementation is heavily dependent on scoplib format instead of openscop format.
- Our existing implementation uses scoplib format for convenience (rather than openscop)
 - No support for access relations in scoplib format (to the best of our knowledge)

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Backup - References

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