COMP 322: Fundamentals of Parallel Programming

Lecture 33: Task Affinity with Places

Vivek Sarkar
Department of Computer Science, Rice University
vsarkar@rice.edu

https://wiki.rice.edu/confluence/display/PARPROG/COMP322
Worksheet #32: MPI Gather

1. MPI.Init(args);
2. int myrank = MPI.COMM_WORLD.Rank();
3. int numProcs = MPI.COMM_WORLD.Size();
4. int size = ...;
5. int[] sendbuf = new int[size];
6. int[] recvbuf = new int[???];
7. . . . // Each process initializes sendbuf
8. MPI.COMM_WORLD.Gather(sendbuf, 0, size, MPI.INT,
9. recvbuf, 0, size, MPI.INT,
10. 0/*root*/);
11. . . .
12. MPI.Finalize();

Question: In the space below, indicate what values should be provided instead of ??? in line 6, and why.
Answer:
recvbuf should be allocated with numProcs*size elements for Gather. Since recvbuf also needs to be allocated in the root, line 6 can be replaced by:
6. int[] recvbuf = (myrank==0) ? new int[numProcs*size] : null;
MapReduce Execution  
(Recap from Lecture 8)

Fine granularity tasks: many more map tasks than machines

Bucket sort to get same keys together

2000 servers => 200,000 Map Tasks, 5,000 Reduce tasks
PseudoCode for WordCount
(Recap from Lecture 8)

1. map(String input_key, String input_value):
2.   // input_key: document name
3.   // input_value: document contents
4.   for each word w in input_value:
5.       EmitIntermediate(w, "1"); // Produce count of words
6. 
7. reduce(String output_key, Iterator intermediate_values):
8.   // output_key: a word
9.   // intermediate_values: a list of counts
10.  int result = 0;
11.   for each v in intermediate_values:
12.      result += ParseInt(v); // get integer from key-value
13.    Emit(AsString(result));
An example Memory Hierarchy --- what is the cost of a Memory Access?

- **L0**: CPU registers hold words retrieved from L1 cache
- **L1**: L1 cache holds cache lines retrieved from L2 cache
- **L2**: L2 cache holds cache lines retrieved from main memory
- **L3**: Main memory holds disk blocks retrieved from local disks
- **L4**: Local secondary storage (local disks) holds files retrieved from disks on remote network servers
- **L5**: Remote secondary storage (tapes, distributed file systems, Web servers)

- Smaller, faster, costlier per byte
- Larger, slower, cheaper per byte

# Storage Trends

## SRAM

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<tr>
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</thead>
<tbody>
<tr>
<td>$/MB</td>
<td>19,200</td>
<td>2,900</td>
<td>320</td>
<td>256</td>
<td>100</td>
<td>75</td>
<td>60</td>
<td>320</td>
</tr>
<tr>
<td>access (ns)</td>
<td>300</td>
<td>150</td>
<td>35</td>
<td>15</td>
<td>3</td>
<td>2</td>
<td>1.5</td>
<td>200</td>
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## DRAM

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<tbody>
<tr>
<td>$/MB</td>
<td>8,000</td>
<td>880</td>
<td>100</td>
<td>30</td>
<td>1</td>
<td>0.1</td>
<td>0.06</td>
<td>130,000</td>
</tr>
<tr>
<td>access (ms)</td>
<td>375</td>
<td>200</td>
<td>100</td>
<td>70</td>
<td>60</td>
<td>50</td>
<td>40</td>
<td>9</td>
</tr>
<tr>
<td>typical size (MB)</td>
<td>0.064</td>
<td>0.256</td>
<td>4</td>
<td>16</td>
<td>64</td>
<td>2,000</td>
<td>8,000</td>
<td>125,000</td>
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</table>

## Disk

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</tr>
</thead>
<tbody>
<tr>
<td>$/MB</td>
<td>500</td>
<td>100</td>
<td>8</td>
<td>0.30</td>
<td>0.01</td>
<td>0.005</td>
<td>0.0003</td>
<td>1,600,000</td>
</tr>
<tr>
<td>access (ms)</td>
<td>87</td>
<td>75</td>
<td>28</td>
<td>10</td>
<td>8</td>
<td>4</td>
<td>3</td>
<td>29</td>
</tr>
<tr>
<td>typical size (MB)</td>
<td>1</td>
<td>10</td>
<td>160</td>
<td>1,000</td>
<td>20,000</td>
<td>160,000</td>
<td>1,500,000</td>
<td>1,500,000</td>
</tr>
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Cache Memories

- **Cache memories** are small, fast SRAM-based memories managed automatically in hardware.
  — Hold frequently accessed blocks of main memory
- CPU looks first for data in caches (e.g., L1, L2, and L3), then in main memory.
- Typical system structure:

[Diagram of CPU chip with cache memories, register file, ALU, bus interface, I/O bridge, system bus, memory bus, and main memory.]
Examples of Caching in the Hierarchy

Ideally one would desire an indefinitely large memory capacity such that any particular word would be immediately available. ... We are forced to recognize the possibility of constructing a hierarchy of memories, each of which has greater capacity than the preceding but which is less quickly accessible.

A. W. Burks, H. H. Goldstine, and J. von Neumann
Preliminary Discussion of the Logical Design of an Electronic Computing Instrument (1946)

**Ultimate goal:** create a large pool of storage with average cost per byte that approaches that of the cheap storage near the bottom of the hierarchy, and average latency that approaches that of fast storage near the top of the hierarchy.

Source: http://www.cs.cmu.edu/afs/cs/academic/class/15213-f10/www/lectures/09-memory-hierarchy.pptx
Locality

• Principle of Locality:
  — Empirical observation: Programs tend to use data and instructions with addresses near or equal to those they have used recently

• Temporal locality:
  — Recently referenced items are likely to be referenced again in the near future

• Spatial locality:
  — Items with nearby addresses tend to be referenced close together in time
  — A Java programmer can only influence spatial locality at the intra-object level
    — The garbage collector and memory management system determines inter-object placement

Locality Example

- **Data references**
  - Reference array elements in succession (stride-1 reference pattern).
  - Reference variable `sum` each iteration.

- **Instruction references**
  - Reference instructions in sequence.
  - Cycle through loop repeatedly.

```
sum = 0;
for (i = 0; i < n; i++)
    sum += a[i];
return sum;
```

Memory Hierarchy in a Multicore Processor

- Memory hierarchy for a single Intel Xeon Quad-core E5440 HarperTown processor chip
  — A SUG@R node contains TWO such chips, for a total of 8 cores
Programmer Control of Task Assignment to Processors

• The parallel programming constructs that we’ve studied thus far result in tasks that are assigned to processors *dynamically* by the HJ runtime system
  — Programmer does not worry about task assignment details

• Sometimes, programmer control of task assignment can lead to significant performance advantages due to improved locality

• Motivation for HJ “places”
  — Provide the programmer a mechanism to restrict task execution to a subset of processors for improved locality
  — Current HJlib implementation supports one level of locality via places, but future HJlib versions will support hierarchical places
Places in HJ

HJ programmer defines mapping from HJ tasks to set of places

HJ runtime defines mapping from places to one or more worker Java threads per place

The option

\[
\text{HjSystemProperty}.numPlaces\text{.setProperty}(p);
\text{HjSystemProperty}.numWorkers\text{.setProperty}(w);
\]

when executing an HJ program can be used to specify

- \( p \), the number of places
- \( w \), the number of worker threads per place

we will abbreviate this as \( p:w \)
Example of 4:2 option on an 8-core node (4 places w/ 2 workers per place)

Core A
- L1
- L2 unified cache

Core B
- L1
- L2 unified cache

Core C
- L1
- L2 unified cache

Core D
- L1
- L2 unified cache

Core E
- L1
- L2 unified cache

Core F
- L1
- L2 unified cache

Place 0

Place 1

Place 2

Place 3
Places in HJlib

\texttt{here()} = place at which current task is executing

\texttt{numPlaces()} = total number of places (runtime constant)

\hspace{1cm} Specified by value of \texttt{p} in runtime option:
\hspace{1cm} \texttt{HjSystemProperty.numPlaces.set Property(p)};

\texttt{place(i)} = place corresponding to index \texttt{i}

\texttt{<place-expr>.toString()} returns a string of the form “place(id=0)”

\texttt{<place-expr>.id()} returns the id of the place as an int

asyncAt(P, () -> S)

\hspace{1cm} Creates new task to execute statement \texttt{S} at place \texttt{P}

\hspace{1cm} \texttt{async()} \rightarrow \texttt{S} is equivalent to \texttt{asyncAt(here(), () -> S)}

\hspace{1cm} \texttt{Main program task starts at place(0)}

Note that \texttt{here()} in a child task refers to the place \texttt{P} at which the child task is executing, not the place where the parent task is executing.
Example of 4:2 option on an 8-core node
(4 places w/ 2 workers per place)

// Main program starts at place 0
asyncAt(place(0), () -> S1);
asyncAt(place(0), () -> S2);
asyncAt(place(1), () -> S3);
asyncAt(place(1), () -> S4);
asyncAt(place(1), () -> S5);
asyncAt(place(2), () -> S6);
asyncAt(place(2), () -> S7);
asyncAt(place(2), () -> S8);
asyncAt(place(3), () -> S9);
asyncAt(place(3), () -> S10);
Example of 1:8 option (1 place w/ 8 workers per place)

All async’s run at place 0 when there’s only one place!
class T1 {
    final place affinity;
    . . .
    // T1's constructor sets affinity to place where instance was created
    T1() { affinity = here; ... }
    . . .
}

. . .

finish { // Inter-place parallelism
    System.out.println("Parent place=", here); // Parent task's place
    for (T1 a = . . .) {
        async at (a.affinity) { // Execute async at place with affinity to a
            a.foo();
            System.out.println("Child place=", here); // Child task's place
        } // async
    } // for
} // finish

. . .
public void runDistChunkedForkJoin(int iterations, int numChunks, Dist dist) {
    for (int iter = 0; iter < iterations; iter++) {
        finish(() -> {
            forseq (0, numChunks - 1, (jj) -> {
                asyncAt(dist.get(jj), () -> {
                    forseq (getChunk(1, n, numChunks, jj), (j) -> {
                        myNew[j] = (myVal[j-1] + myVal[j+1]) / 2.0;
                    }
                }
            });
        });
        double[] temp = myNew; myNew = myVal; myVal = temp;
    } // for iter
}

• Chunk jj is always executed in the same place for each iter
• Method runDistChunkedForkJoin can be called with different values of distribution parameter d
Analyzing Locality of Fork-Join Iterative Averaging Example with Places

Locality benefits will be realized if all instances of chunk 0 execute on the same core and reuse data from the same cache.

iter = 0

iter = 1

iter = 2
Worksheet #33: impact of distribution on parallel completion time (instead of locality)

Name: ___________________          Netid: ___________________

1. public void sampleKernel(
2.     int iterations, int numChunks, Dist d) {
3.     for (int iter = 0; iter < iterations; iter++) {
4.         finish(() -> {
5.             forseq (0, numChunks - 1, (jj) -> {
6.                 asyncAt(dist.get(jj), () -> {
7.                     perf.doWork(jj);
8.                     // Assume that time to process chunk jj = jj units
9.                 });
10.             });
11.         });
12.     double[] temp = myNew; myNew = myVal; myVal = temp;
13. } // for iter
14. } // sample kernel

• Assume an execution with n places, each place with one worker thread
• Will a block or cyclic distribution for d have a smaller abstract completion time, assuming that all tasks on the same place are serialized?