Lecture 17: Midterm Review

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Worksheet #16:
Reordered Asyncs with One Phaser

Name: ___________________         Netid: ___________________

Task A4 has been moved up to line 6. Does this change the computation graph in slide 7? If so, draw the new computation graph. If not, explain why the computation graph is the same.

No, A4 still needs to wait on A2 and A3 to signal before it can start doA4Phase2().

```java
1. finish () -> {
2.     ph = new Phaser(SIG_WAIT); // mode is SIG_WAIT
3.     asyncPhased(ph.inMode(SIG), () -> {
4.         // A1 (SIG mode)
5.         doA1Phase1(); next(); doA1Phase2(); });
6.     asyncPhased(ph.inMode(HjPhaserMode.WAIT), () -> {
7.         // A4 (WAIT mode)
8.         doA4Phase1(); next(); doA4Phase2(); });
9.     asyncPhased(ph.inMode(SIG_WAIT), () -> {
10.        // A2 (SIG_WAIT mode)
11.        doA2Phase1(); next(); doA2Phase2(); });
12.    asyncPhased(ph.inMode(HjPhaserMode.SIG_WAIT), () -> {
13.        // A3 (SIG_WAIT mode)
14.        doA3Phase1(); next(); doA3Phase2(); });
15. });
```
Async and Finish Statements for Task Creation and Termination (Lecture 1)

async S

• Creates a new child task that executes statement S

finish S

▪ Execute S, but wait until all asyncs in S’s scope have terminated.

```
// T₀ (Parent task)
STMT0;
finish {
  async {
    STMT1; // T₁ (Child task)
  }
  STMT2;  // Continue in T₀
    // Wait for T₁
}
STMT3;  // Continue in T₀
```

```T₁
fork
```

```T₀
join
```

```STMT0  STMT1  STMT2  STMT3
```

STMT0
One Possible Solution to Problem #2 in Worksheet 1 (Parallel Matrix Multiplication)

1. `finish` {
2.   for (int i = 0 ; i < N ; i++)
3.     for (int j = 0 ; j < N ; j++)
4.       async {
5.         for (int k = 0 ; k < N ; k++)
7.       } // async
8. } // finish

This program generates $N^2$ parallel async tasks, one to compute each $C[i][j]$ element of the output array. Additional parallelism can be exploited within the inner $k$ loop, but that would require more changes than inserting async & finish.
A Computation Graph (CG) captures the dynamic execution of a parallel program, for a specific input

CG nodes are “steps” in the program’s execution
  - A step is a sequential subcomputation without any async, begin-finish and end-finish operations

CG edges represent ordering constraints
  - “Continue” edges define sequencing of steps within a task
  - “Spawn” edges connect parent tasks to child async tasks
  - “Join” edges connect the end of each async task to its IEF’s end-finish operations

All computation graphs must be acyclic
  - It is not possible for a node to depend on itself

Computation graphs are examples of “directed acyclic graphs” (dags)
Which statements can potentially be executed in parallel with each other?

1. `finish { // F1`
2. `async A;`
3. `finish { // F2`
4. `async B1;`
5. `async B2;`
6. `} // F2`
7. `B3;`
8. `} // F1`

**Key idea:** If two statements, X and Y, have no path of directed edges from one to the other, then they can run in parallel with each other.
Complexity Measures for Computation Graphs

Define

- **TIME(N)** = execution time of node N
- **WORK(G)** = sum of **TIME(N)**, for all nodes N in CG G
  - **WORK(G)** is the total work to be performed in G
- **CPL(G)** = length of a longest path in CG G, when adding up execution times of all nodes in the path
  - Such paths are called **critical paths**
  - **CPL(G)** is the length of these paths (critical path length, also referred to as the span of the graph)
  - **CPL(G)** is also the smallest possible execution time for the computation graph
Which Computation Graph has more ideal parallelism?

Assume that all nodes have TIME = 1, so WORK = 10 for both graphs.

**Computation Graph 1**

- CPL = 7

**Computation Graph 2**

- CPL = 6
Data Races

A data race occurs on location L in a program execution with computation graph CG if there exist steps (nodes) S1 and S2 in CG such that:

1. S1 does not depend on S2 and S2 does not depend on S1, i.e., S1 and S2 can potentially execute in parallel, and
2. Both S1 and S2 read or write L, and at least one of the accesses is a write.

• A data-race is usually considered an error. The result of a read operation in a data race is undefined. The result of a write operation is undefined if there are two or more writes to the same location.

  • Note that our definition of data race includes the case that both S1 and S2 write the same value in location L, even if that may not be considered an error.

• Above definition includes all “potential” data races i.e., we consider it to be a data race even if S1 and S2 end up executing on the same processor.
One Possible Solution to Worksheet 2
(Reverse Engineering a Computation Graph)

Observations:
• Any node with out-degree > 1 must be an async (must have an outgoing spawn edge)
• Any node with in-degree > 1 must be an end-finish (must have an incoming join edge)
• Adding or removing transitive edges does not impact ordering constraints

1. A();
2. finish { // F1
3. async D();
4. B();
5. async {
6. E();
7. finish { // F2
8. async H();
9. F();
10. } // F2
11. G();
12. }
13. } // F1
14. C();
Combine lower and upper bounds to get

$$\max(\text{WORK}(G)/P, \text{CPL}(G)) \leq T_P \leq \frac{\text{WORK}(G)}{P} + \text{CPL}(G)$$

**Corollary 1:** Any greedy scheduler achieves execution time $T_P$ that is within a factor of 2 of the optimal time (since $\max(a,b)$ and $(a+b)$ are within a factor of 2 of each other, for any $a \geq 0, b \geq 0$).

**Corollary 2:** Lower and upper bounds approach the same value whenever

- There’s lots of parallelism, $\frac{\text{WORK}(G)}{\text{CPL}(G)} \gg P$
- Or there’s little parallelism, $\frac{\text{WORK}(G)}{\text{CPL}(G)} \ll P$
Abstract Performance Metrics

- **Basic Idea**
  - Count operations of interest, as in big-O analysis, to evaluate parallel algorithms
  - Abstraction ignores many overheads that occur on real systems

- **Calls to doWork()**
  - Programmer inserts calls of the form, `doWork(N)`, within a step to indicate abstraction execution of N application-specific abstract operation
    - e.g., in the Homework 1 programming assignment (Parallel Sort), we will include one call to `doWork(1)` in each call to `compareTo()`, and ignore the cost of everything else

- Abstract metrics are enabled by calling `HjSystemProperty.abstractMetrics.set(true)` at start of program execution

- If an HJ program is executed with this option, abstract metrics can be printed at end of program execution with calls to `abstractMetrics().totalWork()`, `abstractMetrics().criticalPathLength()`, and `abstractMetrics().idealParallelism()`
One Possible Solution to Worksheet 3 (Multiprocessor Scheduling)

There are 4 idle slots in this schedule — can we do better than $T_2 = 15$?

- As before, $\text{WORK} = 26$ and $\text{CPL} = 11$ for this graph
- $T_2 = 15$, for the 2-processor schedule on the right
- We can also see that $\max(\text{CPL, WORK}/2) \leq T_2 < \text{CPL} + \text{WORK}/2$
Solution to Worksheet 4

- Estimate $T(S,P) \sim \text{WORK}(G,S)/P + \text{CPL}(G,S) = (S-1)/P + \log_2(S)$ for the parallel array sum computation shown in slide 4.

- Assume $S = 1024 \implies \log_2(S) = 10$

- Compute for 10, 100, 1000 processors
  - $T(P) = 1023/P + 10$
  - $\text{Speedup}(10) = T(1)/T(10) = 1033/112.3 \approx 9.2$
  - $\text{Speedup}(100) = T(1)/T(100) = 1033/20.2 \approx 51.1$
  - $\text{Speedup}(1000) = T(1)/T(1000) = 1033/11.0 \approx 93.7$

- Why does the speedup not increase linearly in proportion to the number of processors?
  - Because of the critical path length, $\log_2(S)$, is a bottleneck
Worksheet 4 - Speedup Chart (linear scale)

\[ \text{speedup} = \frac{1033}{\left( \frac{1023}{P} \right) + 10} \]
Example Scenario (PseudoCode)

// Parent task creates child async task
future<Integer> container = future { return computeSum(X, low, mid); };
...
// Later, parent examines the return value
Integer sum = container.get();

Two issues to be addressed:

1) Distinction between container and value in container (box)
2) Synchronization to avoid race condition in container accesses
Worksheet #5 solution: Computation Graphs for Async-Finish and Future Constructs

1) Can you write pseudocode with `async-finish` constructs that generates a Computation Graph with the same ordering constraints as the graph on the right? If so, provide a sketch of the program.

No. Finish cannot be used to ensure that D waits for both B and C, while E waits only for C.

2) Can you write pseudocode with `future async-get` constructs that generates a Computation Graph with the same ordering constraints as the graph on the right? If so, provide a sketch of the program.

Yes, see program sketch with void futures. A dummy return value can also be used.
Worksheet #5 solution (contd)

1. HjFuture<String> A = future(() -> {
   return "A"; });
2. HjFuture<String> B = future(() -> {
   A.get(); return "B"; });
3. HjFuture<String> C = future(() -> {
   A.get(); return "C"; });
4. HjFuture<String> D = future(() -> {
   // Order of B.get() & C.get() doesn’t matter
   B.get(); C.get(); return "D"; });
5. HjFuture<String> E = future(() -> {
   C.get(); return "E"; });
6. HjFuture<String> F = future(() -> {
   D.get(); E.get(); return "F"; });
7. F.get();
Memoization (Lecture 6)

• Memoization - saving and reusing previously computed values of a function rather than recomputing them
  • A optimization technique with space-time tradeoff
• A function can only be memoized if it is referentially transparent, i.e. functional
• Related to caching
  • memoized function "remembers" the results corresponding to some set of specific inputs
  • memoized function populates its cache of results transparently on the fly, as needed, rather than in advance

Helpful Link: http://en.wikipedia.org/wiki/Memoization
Worksheet #6 solution: Parallelizing Pascal’s Triangle with Futures and Memoization

There are four variants of the Binomial Coefficients program provided in four different HJlib methods in the next page:

a. Sequential Recursive without Memoization (chooseRecursiveSeq())
b. Parallel Recursive without Memoization (chooseRecursivePar())
c. Sequential Recursive with Memoization (chooseMemoizedSeq())
d. Parallel Recursive with Memoization (chooseMemoizedPar())

Your task is to analyze the WORK, CPL, and Ideal Parallelism for these four versions, for the input \( N = 4 \), \( K = 2 \). Assume that each call to \( \text{ComputeSum()} \) has COST = 1, and all other operations are free.

Complete all entries in the table:

<table>
<thead>
<tr>
<th>Variant</th>
<th>Work</th>
<th>CPL</th>
<th>Ideal Parallelism</th>
</tr>
</thead>
<tbody>
<tr>
<td>chooseRecursiveSeq</td>
<td>5</td>
<td>5</td>
<td>1</td>
</tr>
<tr>
<td>chooseRecursivePar</td>
<td>5</td>
<td>3</td>
<td>5/3 = 1.67</td>
</tr>
<tr>
<td>chooseMemoizedSeq</td>
<td>4</td>
<td>4</td>
<td>1</td>
</tr>
<tr>
<td>chooseMemoizedPar</td>
<td>4</td>
<td>3</td>
<td>4/3 = 1.33</td>
</tr>
</tbody>
</table>

Do you agree with the following statement: “Parallelization of inefficient algorithms often leads to more ideal parallelism than parallelization of efficient algorithms” in the context of this worksheet?
REMINDER: computation structure of $C(4, 2)$

Nodes with calls to ComputeSum() are in red

$C(n, k) = C(n - 1, k - 1) + C(n - 1, k)$

$C(4, 2) = 6$

$C(3, 1) = 3$

$C(3, 2) = 3$

$C(2, 1) = 2$

$C(2, 2) = 1$

$C(1, 0) = 1$

$C(1, 1) = 1$

$C(n, k) = \binom{n}{k}$
Extending Finish Construct with “Finish Accumulators” (Lecture 7)

• Creation

   accumulator ac = newFinishAccumulator(operator, type);

   — Operator must be associative and commutative (creating task “owns” accumulator)

• Registration

   finish (ac1, ac2, ...) { ... }

   — Accumulators ac1, ac2, ... are registered with the finish scope

• Accumulation

   ac.put(data);

   — Can be performed in parallel by any statement in finish scope that registers ac. Note that a put contributes to the accumulator, but does not overwrite it.

• Retrieval

   ac.get(); // Deterministic operation

   — Returns initial value if called before end-finish, or final value after end-finish

   — get() is nonblocking because no synchronization is needed (finish provides the necessary synchronization)
1. Non-owner task cannot access accumulator outside registered finish

   // T1 allocates accumulator a

   accumulator a = newFinishAccumulator(...);

   a.put(1); // T1 can access a

   async { // T2 cannot access a
     a.put(1); Number v1 = a.get();
   }

2. Non-owner task cannot register accumulator with a finish

   // T1 allocates accumulator a

   accumulator a = newFinishAccumulator(...);

   async {
     // T2 cannot register a with finish

     finish (a) {
       async a.put(1);
     }
   }
Worksheet #7 solution: 
Associativity and Commutativity

Recap:
A binary function f is associative if \( f(f(x,y),z) = f(x,f(y,z)) \).
A binary function f is commutative if \( f(x,y) = f(y,x) \).

Worksheet problems:
1) Claim: a Finish Accumulator (FA) can only be used with operators that are associative and commutative. Why? What can go wrong with accumulators if the operator is non-associative or non-commutative?
You may get different answers in different executions if the operator is non-associative or non-commutative e.g., an accumulator can be implemented using one “partial accumulator” per processor core.
2) For each of the following functions, indicate if it is associative and/or commutative.
a) \( f(x,y) = x+y \), for integers \( x, y \), is associative and commutative
b) \( g(x,y) = (x+y)/2 \), for integers \( x, y \), is commutative but not associative
⇒ Incorrect answers found in some worksheets: Associative / Both / Neither
c) \( h(s1,s2) = \text{concat}(s1, s2) \) for strings \( s1, s2 \), e.g., \( h(“ab”,”cd”) = “abcd” \), is associative but not commutative
⇒ Incorrect answers found in some worksheets: Commutative / Neither
Map Reduce: Summary (Lecture 8)

• Input set is of the form \{((k_1, v_1), \ldots (k_n, v_n))\}, where \((k_i, v_i)\) consists of a key, \(k_i\), and a value, \(v_i\).
  • Assume that the key and value objects are immutable, and that equality comparison is well defined on all key objects.

• Map function \(f\) generates sets of intermediate key-value pairs, \(f(k_i,v_i) = \{(k_1', v_1'), \ldots (k_m', v_m')\}\). The \(k_j'\) keys can be different from \(k_i\) key in the input of the map function.
  • Assume that a flatten operation is performed as a post-pass after the map operations, so as to avoid dealing with a set of sets.

• Reduce operation groups together intermediate key-value pairs, \(\{(k', v_j')\}\) with the same \(k'\), and generates a reduced key-value pair, \((k', v'')\), for each such \(k'\), using reduce function \(g\).
Analyze the total WORK and CPL for the Map reduce example:

- Assume that each Map step has WORK = number of input words, and CPL = 1
- Assume that each Reduce step has WORK = number of input word-count pairs, and CPL = \( \log_2(\# \text{ occurrences for input word with largest } \# \text{ pairs}) \)

**WORK/CPL for all Map steps:**
- WORK = 15
- CPL = 1 (ignore impact of local sums on CPL)

**WORK/CPL for Reduce 1 step:**
- WORK = 5
- CPL = ceiling(\( \log_2(4) \)) = 2

**WORK/CPL for Reduce 2 step:**
- WORK = 4
- CPL = ceiling(\( \log_2(3) \)) = 2

**Total WORK and CPL**
- WORK = 15 + 5 + 4 = 24
- CPL = 1 + 2 = 3
Functional vs. Structural Determinism (Lecture 9)

• A parallel program is said to be *functionally deterministic* if it always computes the same answer when given the same input.

• A parallel program is said to be *structurally deterministic* if it always produces the same computation graph when given the same input.

• **Data-Race-Free Determinism Property**
  — If a parallel program is written using the constructs learned so far (finish, async, futures) and is known to be data-race-free, *then it must be both functionally deterministic and structurally deterministic.*
Worksheet #9: Classifying different versions of parallel search algorithm

Enter “YES” or “NO”, as appropriate, in each box below

<table>
<thead>
<tr>
<th>Example: String Search variation</th>
<th>Data Race Free?</th>
<th>Functionally Deterministic?</th>
<th>Structurally Deterministic?</th>
</tr>
</thead>
<tbody>
<tr>
<td>V1: Count of all occurrences</td>
<td>YES</td>
<td>YES</td>
<td>YES</td>
</tr>
<tr>
<td>V2: Existence of an occurrence</td>
<td>NO</td>
<td>YES</td>
<td>YES</td>
</tr>
<tr>
<td>V3: Index of any occurrence</td>
<td>NO</td>
<td>NO</td>
<td>YES</td>
</tr>
<tr>
<td>V4: Optimized existence of an occurrence: do not create more async tasks after occurrence is found</td>
<td>NO</td>
<td>YES</td>
<td>NO</td>
</tr>
<tr>
<td>V5: Optimized index of any occurrence: do not create more async tasks after occurrence is found</td>
<td>NO</td>
<td>NO</td>
<td>NO</td>
</tr>
</tbody>
</table>
One-Dimensional Iterative Averaging Example (Lecture 11)

- Initialize a one-dimensional array of \((n+2)\) double’s with boundary conditions, \(\text{myVal}[0] = 0\) and \(\text{myVal}[n+1] = 1\).

- In each iteration, each interior element \(\text{myVal}[i]\) in \(1..n\) is replaced by the average of its left and right neighbors.
  - Two separate arrays are used in each iteration, one for old values and the other for the new values.

- After a sufficient number of iterations, we expect each element of the array to converge to \(\text{myVal}[i] = (\text{myVal}[i-1] + \text{myVal}[i+1])/2\), for all \(i\) in \(1..n\).

Illustration of an intermediate step for \(n = 8\) (source: Figure 6.19 in Lin-Snyder book)
HJ code for One-Dimensional Iterative Averaging using nested forseq-forall structure

1. float[] myVal = new float[n+2];
2. float[] myNew = new float[n+2];
3. … // Intialize myVal, m, n
4. forseq(0, m-1, (iter) -> {
5.   // Compute MyNew as function of input array MyVal
6.   forall(1, n, (j) -> {
7.     myNew[j] = (myVal[j-1] + myVal[j+1])/2.0;
8.   }); // forall
9.   // What is the purpose of line 10 below?
10.  float[] temp=myVal; myVal=myNew; myNew=temp;
11. });// for
12. }); // for
1) Assuming n=9 and the input array below, perform a “half-iteration” of the iterative averaging example by only filling in the blanks for odd values of j in the myNew[] array (different from the real algorithm). Recall that the computation is “myNew[j] = (myVal[j-1] + myVal[j+1])/2.0;”

<table>
<thead>
<tr>
<th>index, j</th>
<th>0</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
<th>7</th>
<th>8</th>
<th>9</th>
<th>10</th>
</tr>
</thead>
<tbody>
<tr>
<td>myVal</td>
<td>0</td>
<td>0</td>
<td>0.2</td>
<td>0</td>
<td>0</td>
<td>0.4</td>
<td>0</td>
<td>0</td>
<td>0.8</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>myNew</td>
<td>0</td>
<td>0.1</td>
<td>0.2</td>
<td>0.3</td>
<td>0.4</td>
<td>0.5</td>
<td>0.6</td>
<td>0.7</td>
<td>0.8</td>
<td>0.9</td>
<td>1</td>
</tr>
</tbody>
</table>

2) Will the contents of myVal[] and myNew[] change in further iterations? No, this represents the converged value (equilibrium/fixpoint).

3) Write the formula for the final value of myNew[i] as a function of i and n. In general, this is the value that we will get if m (= #iterations in sequential for-iter loop) is large enough.

After a sufficiently large number of iterations, the iterated averaging code will converge with myNew[i] = myVal[i] = i / (n+1)
Barriers (Lecture 12)

• Question: how can we transform this code so as to ensure that all tasks say hello before any tasks say goodbye, without having to change local?

• Approach 2: insert a “barrier” (“next” statement) between the hello’s and goodbye’s

1. // APPROACH 2
2. forallPhased (0, m - 1, (i) -> {
3.   int sq = i*i;
4.   System.out.println(“Hello from task with square = “ + sq);
5.   next(); // Barrier
6.   System.out.println(“Goodbye from task with square = “ + sq);
7. });

• next ➔ each forallPhased iteration waits at barrier until all iterations arrive (previous phase is completed), after which the next phase can start

—Scope of next is the closest enclosing forallPhased statement
—If a forallPhased iteration terminates before executing “next”, then the other iterations don’t wait for it
Draw a “barrier matching” figure similar to lecture 12 slide 11 for the code fragment below.

1. `String[] a = { "ab", "cde", "f" };`
2. `... int m = a.length; ...
3. `forallPhased (0, m-1, (i) -> {
4. `for (int j = 0; j < a[i].length(); j++) {
5. `// forall iteration i is executing phase j
6. `System.out.println("(" + i + "," + j + ")");
7. `next();
8. `});

Solution
Converting forseq-forall version into a forall-forseq version with barriers (Lecture 14)

1. double[] gVal = new double[n+2]; gVal[n+1] = 1;
2. double[] gNew = new double[n+2];
3. forallPhased(1, n, (j) -> {
   // Create n tasks
4.   // Initialize myVal and myNew as local pointers
5.   double[] myVal = gVal; double[] myNew = gNew;
6.   forseq(0, m-1, (iter) -> {
7.     // Compute MyNew as function of input array MyVal
8.     myNew[j] = (myVal[j-1] + myVal[j+1])/2.0;
9.     next(); // Barrier before next iteration of iter loop
10.    // Swap local pointers, myVal and myNew
11.    double[] temp=myVal; myVal=myNew; myNew=temp;
12.    // myNew becomes input array for next iteration
13.   }); // forseq
14. }); // forall
Answer the questions in the table below for the versions of the Iterative Averaging code shown in slides 7, 8, 10, 12. Write in your answers as functions of $m$, $n$, and $nc$.

<table>
<thead>
<tr>
<th></th>
<th>Slide 7</th>
<th>Slide 8</th>
<th>Slide 10</th>
<th>Slide 12</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>How many tasks are created</strong></td>
<td>$m*n$</td>
<td>$n$</td>
<td>$m*nc$</td>
<td>$nc$</td>
</tr>
<tr>
<td><strong>(excluding the main program task)</strong></td>
<td>Incorrect: $n * m$</td>
<td>Incorrect: $n * nc$</td>
<td>Incorrect: $n<em>m, m</em>nc$</td>
<td></td>
</tr>
<tr>
<td><strong>How many barrier operations</strong></td>
<td>$0$</td>
<td>$m$</td>
<td>$0$</td>
<td>$m$</td>
</tr>
<tr>
<td><strong>(calls to next per task) are</strong></td>
<td>Incorrect: $m$</td>
<td>Incorrect: $m*n$</td>
<td>Incorrect: $m*nc, nc$</td>
<td></td>
</tr>
<tr>
<td><strong>performed?</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The SPMD version in slide 12 is the most efficient because it only creates $nc$ tasks. (Task creation is more expensive than a barrier operation.)
Extending HJ Futures for Macro-Dataflow: Data-Driven Futures (Lecture 15)

```
HjDataDrivenFuture<T1> ddfA = newDataDrivenFuture();
```

- Allocate an instance of a data-driven-future object (container)
- Object in container must be of type T1, and can only be assigned once via put() operations
- HjDataDrivenFuture extends the HjFuture interface

```
ddfA.put(V);
```

- Store object V (of type T1) in ddfA, thereby making ddfA available
- Single-assignment rule: at most one put is permitted on a given DDF
asyncAwait(ddfA, ddfB, ..., () -> Stmt);

- Create a new data-driven-task to start executing Stmt after all data-driven futures ddfA, ddfB, ... become available (i.e., after task becomes “enabled”)
- Await clause can be used to implement “nodes” and “edges” in a computation graph

ddfA.get()

- Return value (of type T1) stored in ddfA
- Throws an exception if put() has not been performed
  — Should be performed by async’s that contain ddfA in their await clause, or if there’s some other synchronization to guarantee that the put() was performed
For the example below, will reordering the five async statements change the meaning of the program (assuming that the semantics of the reader/writer methods depends only on their parameters)? If so, show two orderings that exhibit different behaviors. If not, explain why not.

No, reordering the asyncs doesn’t change the meaning of the problem. Regardless of the order, Task 3 will always wait on Task 1. Task 5 will always wait on Task 2. Task 4 will always wait on both Task 1 and 2.

1. DataDrivenFuture left = new DataDrivenFuture();
2. DataDrivenFuture right = new DataDrivenFuture();
3. finish {
4.    async await(left) leftReader(left); // Task3
5.    async await(right) rightReader(right); // Task5
6.    async await(left,right)
7.        bothReader(left,right); // Task4
8.    async left.put(leftWriter()); // Task1
9.    async right.put(rightWriter()); // Task2
10. }

Name: ___________________        Netid: ___________________
Summary of Phaser Construct  
(Lecture 15)

- Phaser allocation
  - HjPhaser ph = new Phaser(mode);
  - Phaser ph is allocated with registration mode
  - Phaser lifetime is limited to scope of Immediately Enclosing Finish (IEF)

- Registration Modes
  - HjPhaserMode.SIG, HjPhaserMode.WAIT,
    HjPhaserMode.SIG_WAIT, HjPhaserMode.SIG_WAIT_SINGLE
  - NOTE: phaser WAIT is unrelated to Java wait/notify (which we will study later)

- Phaser registration
  - asyncPhased (ph₁.inMode(<mode₁>), ph₂.inMode(<mode₂>), ... () -> <stmt> )
  - Spawned task is registered with ph₁ in mode₁, ph₂ in mode₂, ...
  - Child task’s capabilities must be subset of parent’s
  - asyncPhased <stmt> propagates all of parent’s phaser registrations to child

- Synchronization
  - next();
  - Advance each phaser that current task is registered on to its next phase
  - Semantics depends on registration mode
  - Barrier is a special case of phaser, which is why next is used for both
Complete the phased clause below to implement the left-right neighbor synchronization shown above.

1. `finish () -> {`
2. `final HjPhaser[] ph =`
   `new HjPhaser[m+2]; // array of phaser objects`
3. `forseq(0, m+1, (i) -> { ph[i] = newPhaser(SIG_WAIT) });`
4. `forseq(1, m, (i) -> {`
5. `asyncPhased(`
   `ph[i-1].inMode(WAIT),`
   `ph[i].inMode(SIG),`
   `ph[i+1].inMode(WAIT), () -> {`
6. `doPhase1(i);`
7. `next();`
8. `doPhase2(i); }); // asyncPhased`
9. `}); // forseq`
10. `}); // finish`

**NOTE:** Task-to-phaser mappings can be many-to-many in general. In general, it is important to understand the difference between computation tasks (async’s) and synchronization objects (phasers).
Summary of Parallel Programming
Constructs you’ve learned so far

• Task Parallelism (Unit 1)
  — Async (task creation)
  — Finish (structured task termination)

• Functional Parallelism (Unit 2)
  — Future (task creation)
  — Future get() (task termination with return value)
  — Accumulators (functional reduction)
  — Map-Reduce (functional parallelism & reduction on key-value pairs)

• Loop Parallelism (Unit 3)
  — Forall (parallel loops)
  — Barriers (all-to-all synchronization)

• Dataflow Parallelism (Unit 4)
  — Data-Driven Tasks (dataflow parallelism)
  — Phasers (point-to-point synchronization)
  — Phaser-specific next operations