COMP 322: Fundamentals of Parallel Programming

Lecture 4: Parallel Speedup and Amdahl's Law

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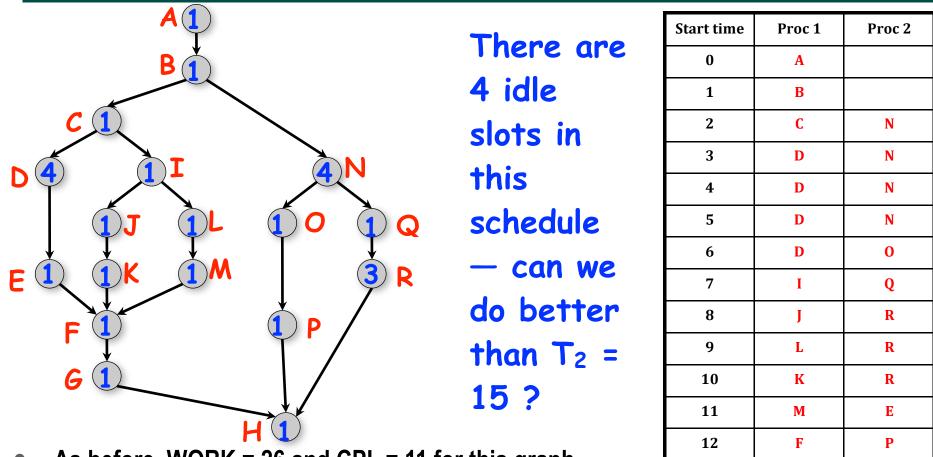
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COMP 322 January 2018





One Possible Solution to Worksheet 3 (Multiprocessor Scheduling)



- As before, WORK = 26 and CPL = 11 for this graph
- T₂ = 15, for the 2-processor schedule on the right
- We can also see that max(CPL,WORK/2) <= T₂ < CPL + WORK/2

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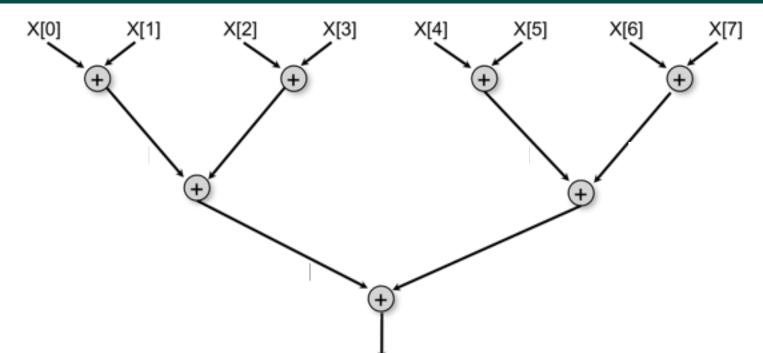


Parallel Speedup

- Define Speedup(P) = T₁ / T_P
 - -Factor by which the use of P processors speeds up execution time relative to 1 processor, for a fixed input size
 - —For ideal executions without overhead, 1 <= Speedup(P) <= P
 - —This is what you will see with abstract metrics, but these bounds may not hold when we start measuring real execution times with real overheads
 - —Linear speedup
 - When Speedup(P) = k*P, for some constant k, 0 < k < 1
- Ideal Parallelism = WORK / CPL = T_1 / T_{∞}
 - = Parallel Speedup on an unbounded (infinite) number of processors



Computation Graph for Recursive Tree approach to computing Array Sum in parallel



Assume greedy schedule, input array size = S is a power of 2, each add takes 1 time unit

- WORK(G) = S-1, and CPL(G) = log2(S)
- Define T(S,P) = parallel execution time for Array Sum with size S on P processors
- Use upper bound T(S,P) <= WORK(G)/P + CPL(G) as a worst-case estimate
 - T(S,P) = WORK(G)/P + CPL(G) = (S-1)/P + log2(S)
- \Rightarrow Speedup(S,P) = T(S,1)/T(S,P) = (S-1)/((S-1)/P + log2(S))



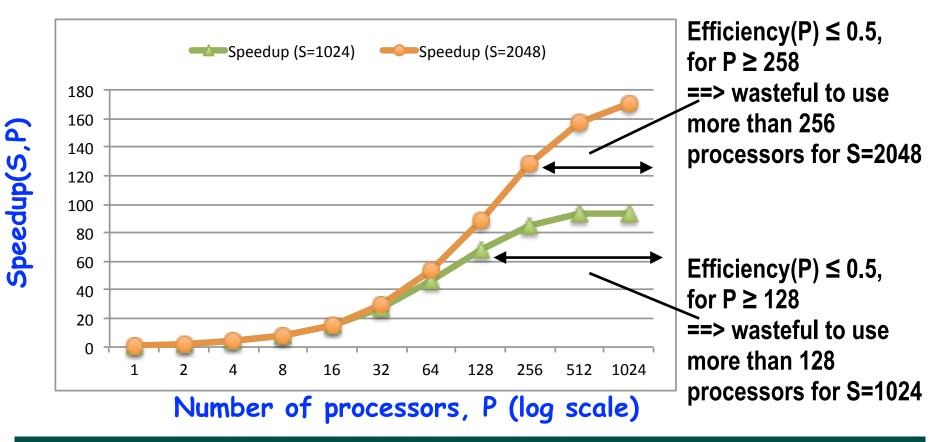
How many processors should we use?

- Define Efficiency(P) = Speedup(P)/ P = T₁/(P * T_P)
 - Processor efficiency --- figure of merit that indicates how well a parallel program uses available processors
 - For ideal executions without overhead, 1/P <= Efficiency(P) <= 1
 - Efficiency(P) = 1 (100%) is the best we can hope for.
- Half-performance metric
 - $-S_{1/2}$ = input size that achieves Efficiency(P) = 0.5 for a given P
 - Figure of merit that indicates how large an input size is needed to obtain efficient parallelism
 - A larger value of $S_{1/2}$ indicates that the problem is harder to parallelize efficiently
- How many processors to use?
 - Common goal: choose number of processors, P for a given input size, S, so that efficiency is at least 0.5 (50%)



ArraySum: Speedup as function of array size, S, and number of processors, P

- Speedup(S,P) = T(S,1)/T(S,P) = (S-1)/((S-1)/P + log₂(S))
- Asymptotically, Speedup(S,P) \rightarrow (S-1)/log₂S, as P \rightarrow infinity







Amdahl's Law [1967]

- If q ≤ 1 is the fraction of WORK in a parallel program that <u>must be executed sequentially</u> for a given input size S, then the best speedup that can be obtained for that program is Speedup(S,P) ≤ 1/q.
- Observation follows directly from critical path length lower bound on parallel execution time
 - -- CPL >= q * T(S,1)
 - T(S,P) >= q * T(S,1)
 - Speedup(S,P) = T(S,1)/T(S,P) <= 1/q</p>
- This upper bound on speedup simplistically assumes that work in program can be divided into sequential and parallel portions
 - Sequential portion of WORK = q
 - also denoted as f_s (fraction of sequential work)
 - Parallel portion of WORK = 1-q
 - also denoted as f_p (fraction of parallel work)
- Computation graph is more general and takes dependences into account

Illustration of Amdahl's Law: Best Case Speedup as function of Parallel Portion

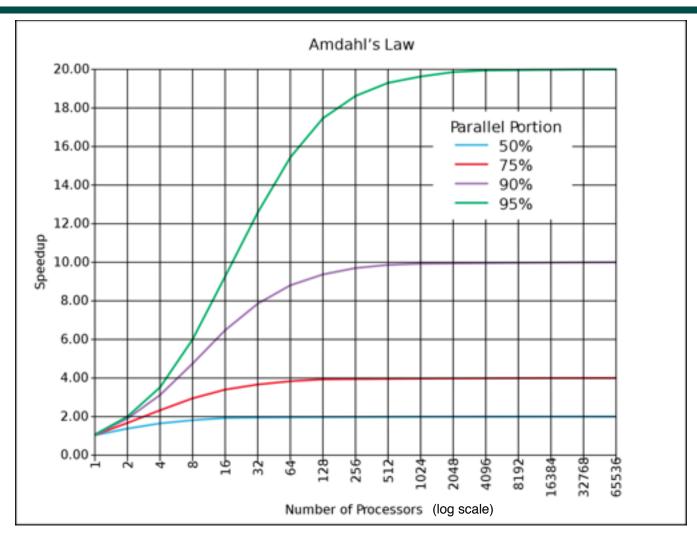


Figure source: <u>http://en.wikipedia.org/wiki/Amdahl</u>'s law



COMP 322, Spring 2018 (M. Joyner, Z. Budimlić)

Announcements & Reminders

- IMPORTANT:
 - -Watch video & read handout for topic 2.1 for next lecture on Monday, Jan 22nd
- HW1 was posted on the course web site (<u>http://comp322.rice.edu</u>) on Jan 10th, and is due on Jan 24th
- Quiz for Unit 1 (topics 1.1 1.5) is due by Jan 26th on Canvas
- Midterm exam will be on Thursday, Feb 22nd. Time is TBD.
- See course web site for all work assignments and due dates
- Use Piazza (public or private posts, as appropriate) for all communications re. COMP 322
- See <u>Office Hours</u> link on course web site for latest office hours schedule.

