COMP 322: Fundamentals of Parallel Programming

Lecture 34: Task Affinity with Places

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Worksheet #33: Combining Task and MPI parallelism

Name: _____ Net ID: _____

Compute the critical path length for the MPI program shown on the right in pseudocode, assuming that it is executed with 2 processes/ranks. (Assume that the send/recv calls in lines 5 & 10 match with each other.)

CPL = 2

```
1. main() {
2.    if (my rank == 0)
3.      finish { // F1
4.        async await(req) doWork(1);
5.        MPI_Irecv(rank 1, ..., req);
6.        doWork(1);
7.    }
8.    else {
9.        doWork(1);
10.        MPI_Send(rank 0, ...);
11.    }
12. } // main
```



Organization of a Distributed-Memory Multiprocessor

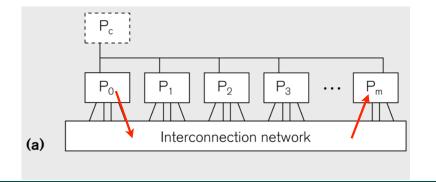
Figure (a)

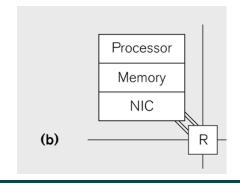
- Host node (P_c) connected to a cluster of processor nodes (P₀ ... P_m)
- Processors P₀ ... P_m communicate via an interconnection network which could be standard TCP/IP (e.g., for Map-Reduce) or specialized for high performance communication (e.g., for scientific computing)

Figure (b)

 Each processor node consists of a processor, memory, and a Network Interface Card (NIC) connected to a router node (R) in the interconnect

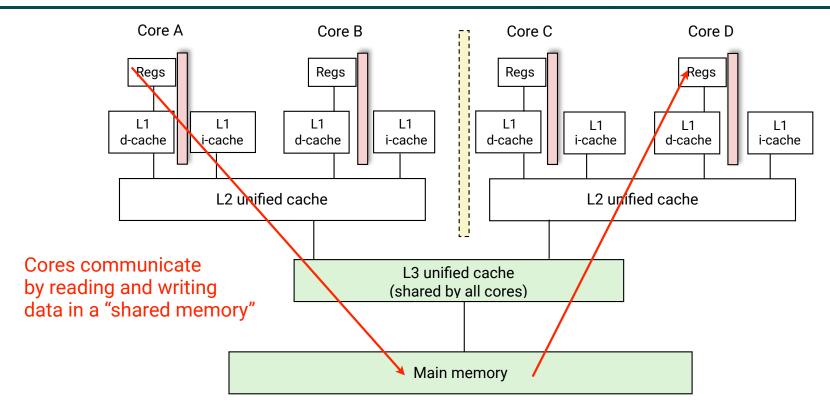
Processors communicate by sending messages via an interconnect







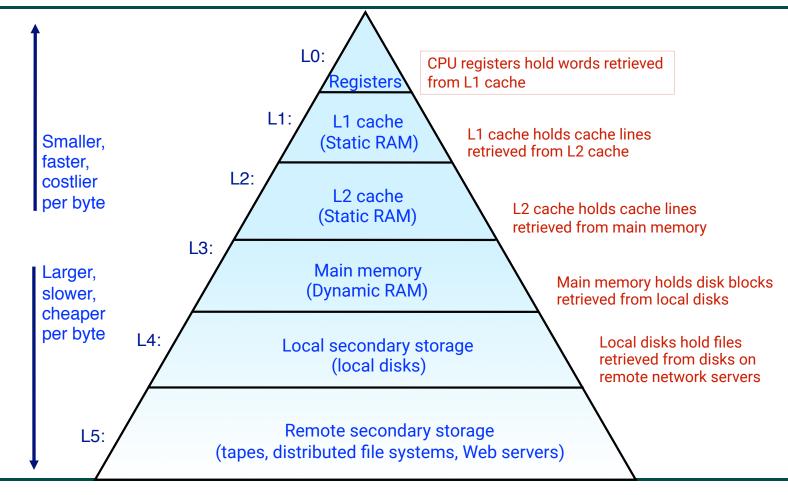
Organization of a Shared-Memory Multicore Symmetric Multiprocessor (SMP)



- Memory hierarchy for a single Intel Xeon (Nehalem) Quad-core processor chip
 - A NOTS node contains TWO 8-core or 12-core E5-2650 v2 Ivy Bridge chips, for a total of 16 or 24 cores



What is the cost of a Memory Access? An example Memory Hierarchy





Storage Trends

SRAM

Metric	1980	1985	1990	1995	2000	2005	2010	2010:1980
\$/MB	19,200	2,900	320	256	100	75	60	320
access (ns)	300	150	35	15	3	2	1.5	200

DRAM

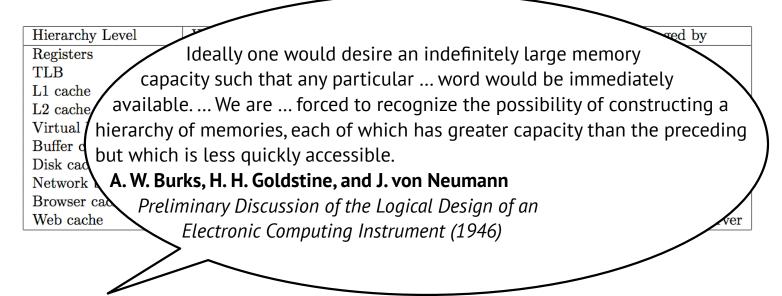
Metric	1980	1985	1990	1995	2000	2005	2010	2010:1980
\$/MB	8,000	880	100	30	1	0.1	0.06	130,000
access (ns)	375	200	100	70	60	50	40	9
typical size (MB)	0.064	0.256	4	16	64	2,000	8,000	125,000

Disk

Metric	1980	1985	1990	1995	2000	2005	2010	2010:1980
\$/MB	500	100	8	0.30	0.01	0.005	0.0003	1,600,000
access (ms)	87	75	28	10	8	4	3	29
typical size (MB)	1	10	160	1,000	20,000	160,000	1,500,000	1,500,000



Examples of Caching in the Hierarchy

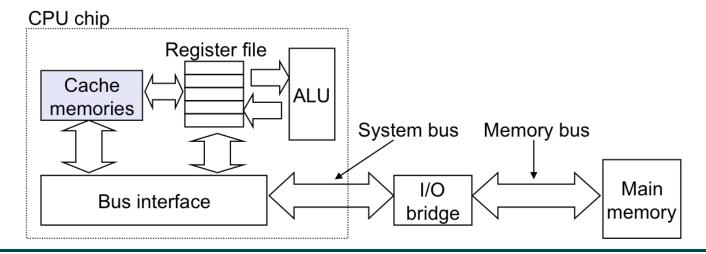


<u>Ultimate goal:</u> create a large pool of storage with average cost per byte that approaches that of the cheap storage near the bottom of the hierarchy, and average latency that approaches that of fast storage near the top of the hierarchy.



Cache Memories

- Cache memories are small, fast SRAM-based memories managed automatically in hardware.
 - Hold frequently accessed blocks of main memory
- CPU looks first for data in caches (e.g., L1, L2, and L3), then in main memory.
- Typical system structure:





Cache Analogy

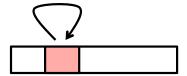
Hungry! must eat!

- —Option 1: go to refrigerator
 - Found? -> eat!
 - Latency = 1 minute
- -Option 2: go to store
 - Found? -> purchase, take home, eat!
 - Latency = 20-30 minutes
- -Option 3: grow food!
 - Plant, wait ... wait ... , harvest, eat!
 - Latency = ~250,000 minutes (~ 6 months)

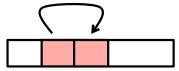


Locality

- Principle of Locality:
 - Empirical observation: Programs tend to use data and instructions with addresses near or equal to those they have used recently
- Temporal locality:
 - Recently referenced items are likely to be referenced again in the near future



- Spatial locality:
 - Items with nearby addresses tend to be referenced close together in time



- A Java programmer can only influence spatial locality at the intra-object level or within arrays
 - The garbage collector and memory management system determines inter-object placement



Locality Example

```
sum = 0;
for (i = 0; i < n; i++)
   sum += a[i];
return sum;</pre>
```

- Data references
 - Reference array elements in succession (stride-1 reference pattern).

Spatial locality

- Reference variable sum each iteration.

Temporal locality

- Instruction references
 - Reference instructions in sequence.

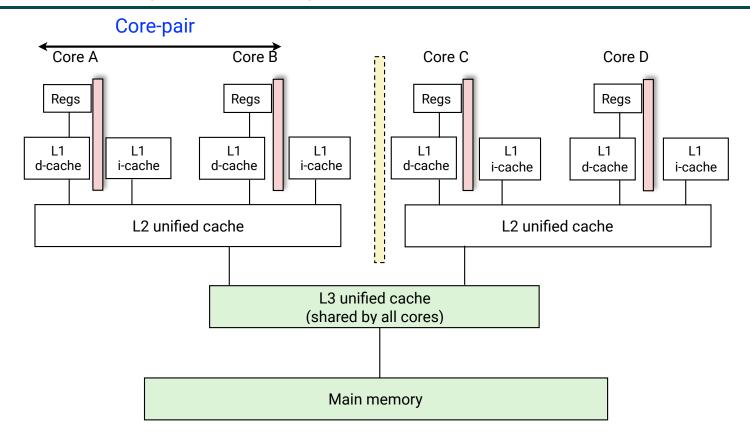
Cycle through loop repeatedly.

Spatial locality

Temporal locality



Memory Hierarchy in a Multicore Processor



Memory hierarchy for a single Intel Xeon (Nehalem) Quad-core processor chip

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Programmer Control of Task Assignment to Processors

- The parallel programming constructs that we've studied thus far result in tasks that are assigned to processors dynamically by the HJ runtime system
 - Programmer does not worry about task assignment details
- Sometimes, programmer control of task assignment can lead to significant performance advantages due to improved locality
- Motivation for HJ "places"
 - Provide the programmer a mechanism to restrict task execution to a subset of processors for improved locality
 - HJlib implementation used in COMP322 supports one level of locality via places, there are more general HJlib versions that support hierarchical places



Places in HJlib

HJ programmer defines mapping from HJ tasks to set of places

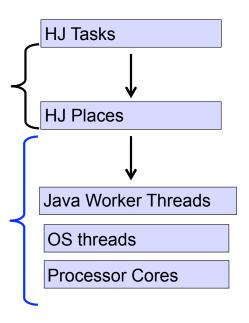
HJ runtime defines mapping from places to one or more worker Java threads per place

```
The API calls
HjSystemProperty.numPlaces.set(p);
HjSystemProperty.numWorkers.set(w);
```

when executing an HJ program can be used to specify

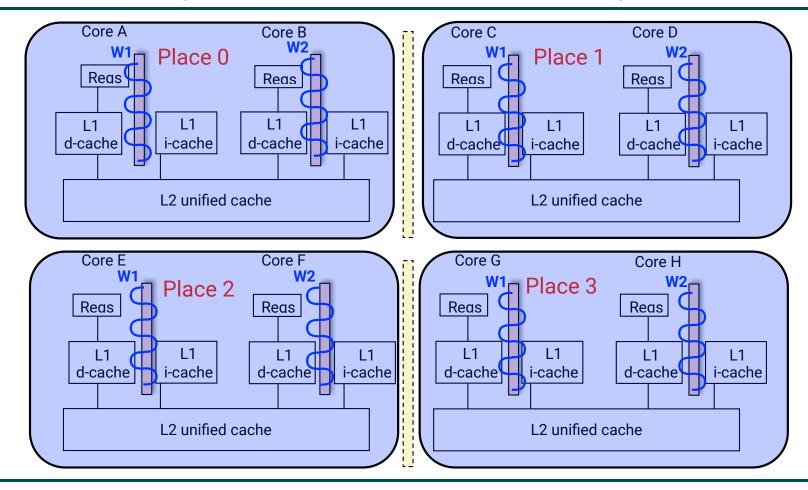
p, the number of places

w, the number of worker threads per place we will abbreviate this as p:w





Example of 4:2 option on an 8-core node (4 places w/ 2 workers per place)





Places in HJlib

```
here() = place at which current task is executing
numPlaces() = total number of places (runtime constant)
    Specified by value of p in runtime option:
    HjSystemProperty.numPlaces.set(p);
place(i) = place corresponding to index i
<place-expr>.toString() returns a string of the form "place(id=0)"
<place-expr>.id() returns the id of the place as an int
asyncAt(P, () -> S)
    Creates new task to execute statement S at place P
    async(() -> S) is equivalent to asyncAt(here(), () -> S)
    Main program task starts at place(0)
```

Note that here() in a child task refers to the place P at which the child task is executing, not the place where the parent task is executing



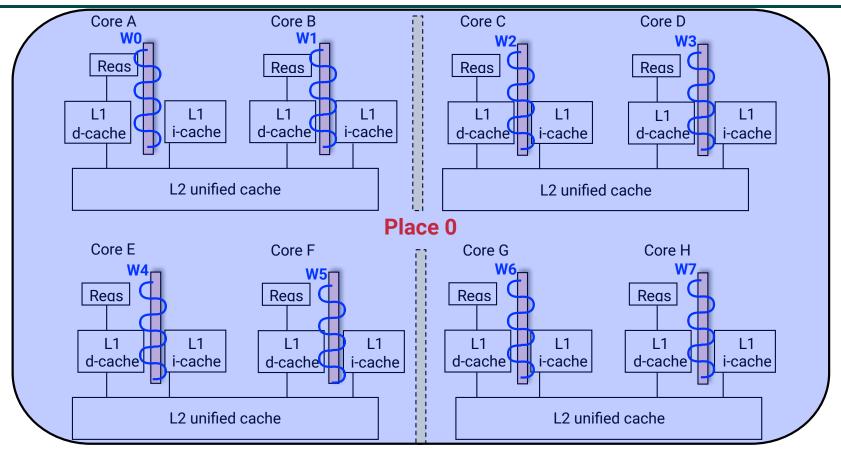
Example of 4:2 option on an 8-core node (4 places w/ 2 workers per place)

```
// Main program starts at place 0
                                                                                                                asyncAt(place(1), () -> S3);
asyncAt(place(0), () -> S1);
                                                                                                                asyncAt(place(1), () -> S4);
asyncAt(place(0), () -> S2);
                                                                                                                asyncAt(place(1), () -> S5);
                                          Core A
                                                              Core B
                                                                                    Core C
                                                                                                        Core D
                                                  Place 0
                                                                                             Place 1
                                          Reas
                                                              Reas
                                                                                   Reas
                                                                                                        Reas
                                          L1
                                                                                     L1
                                                                                              L1
                                                                                                          L1
                                                                                                                   L1
                                                            d-cache
                                                   i-cache
                                                                      i-cache
                                       d-cache
                                                                                   d-cache
                                                                                            i-cache
                                                                                                       d-cache
                                                                                                                 i-cache
                                                   L2 unified cache
                                                                                             L2 unified cache
                                          Core E
                                                              Core F
                                                                                                         Core H
                                                                                            Place 3
                                                   Place 2
                                         Reas
                                                              Reas
                                                                                   Reas
                                                                                                        Reas
                                                     L1
                                                                         L1
                                                                                     L1
                                                                                              L1
                                                                                                                   L1
                                         d-cache
                                                                                   d-cache
                                                                                             i-cache
                                                   i-cache
                                                             d-cache
                                                                       i-cache
                                                                                                       d-cache
                                                                                                                 i-cache
                                                   L2 unified cache
                                                                                             L2 unified cache
 asyncAt(place(2), () \rightarrow S6);
 asyncAt(place(2), () -> S7);
                                                                                                                  asyncAt(place(3), () -> S9);
asyncAt(place(3), () -> S10);
```



asyncAt(place(2), () -> S8);

Example 1:8 option (1 place w/ 8 workers per place)



All async's run at place 0 when there's only one place!



HJ program with places

```
private static class T1 {
1.
      final HjPlace affinity;
2.
      public T1(HjPlace affinity) {
4.
        // set affinity of instance to place where it is created
5.
        this.affinity = here();
7.
8.
      public void foo() { ... }
10.
11.
   finish(() -> {
12.
      println("Parent place: " + here());
13.
      for (T1 a : t10bjects) {
14.
        // Execute async at place with affinity to a
15.
        asyncAt(a.affinity, () -> {
16.
          println("Child place: " + here()); // Child task's place
17.
          a.foo();
18.
        });
19.
20.
   });
21.
```



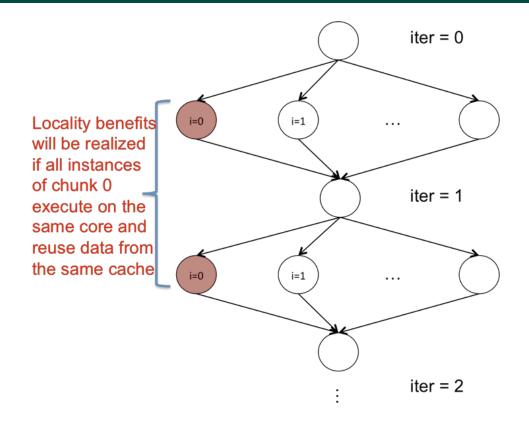
Chunked Fork-Join Iterative Averaging Example with Places

```
public void runDistChunkedForkJoin(
2.
      int iterations, int numChunks, Dist dist) {
3.
      // dist is a user-defined map from int to HiPlace
      for (int iter = 0; iter < iterations; iter++) {
5.
        finish(() -> {
6.
          forseq (0, numChunks - 1, (jj) -> {
            asyncAt(dist.get(jj), () -> {
7.
8.
              forseq (getChunk(1, n, numChunks, jj), (j) -> {
                myNew[i] = (myVal[i-1] + myVal[i+1]) / 2.0;
9.
10.
11.
            });
12.
          });
13.
        });
14.
         double[] temp = myNew; myNew = myVal; myVal = temp;
15.
      } // for iter
16. }
```

- Chunk jj is always executed in the same place for each iter
- Method runDistChunkedForkJoin can be called with different values of distribution parameter dist



Analyzing Locality of Fork-Join Iterative Averaging Example with Places



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Block Distribution

- A block distribution splits the index region into contiguous subregions, one per place, while trying to keep the subregions as close to equal in size as possible.
- Block distributions can improve the performance of parallel loops that exhibit spatial locality across contiguous iterations.
- Example: dist.get(index) for a block distribution on 4 places, when index is in the range, 0...15

Index	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Place id		()			1	L				2			3	}	



Distributed Parallel Loops

- The pseudocode below shows the typical pattern used to iterate over an input region r, while creating one async task for each iteration p at the place dictated by distribution d i.e., at place d.get(p).
- This pattern works correctly regardless of the rank and contents of input region r and input distribution d i.e., it is not constrained to block distributions



Cyclic Distribution

- A cyclic distribution "cycles" through places 0 ... place.MAX PLACES 1 when spanning the input region
- Cyclic distributions can improve the performance of parallel loops that exhibit load imbalance
- Example: dist.get(index) for a cyclic distribution on 4 places, when index is in the range, 0...15

Index	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Place id	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3

