
COMP 322: Fundamentals of Parallel Programming

Lecture 18: Phaser-specific Next Operations, Classification of Parallel Programs

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Solution to Worksheet #17:

Critical Path Length for Computation with

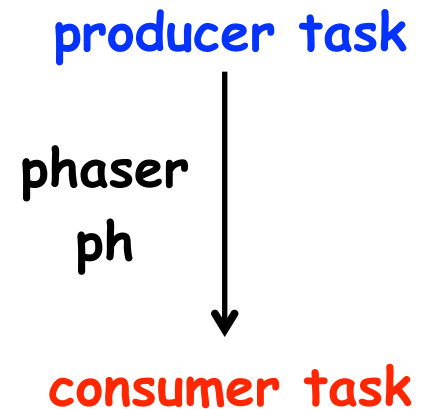
Compute the WORK and CPL values for the program shown below. (WORK = 204, CPL = 102). How would they be different if the `signal()` statement was removed? (CPL would increase to 202.)

```
1. finish(() -> {
2.   final HjPhaser ph = newPhaser(SIG_WAIT);
3.   asyncPhased(ph.inMode(SIG_WAIT), () -> { // Task T1
4.     A(0); doWork(1);    // Shared work in phase 0
5.     signal();
6.     B(0); doWork(100); // Local work in phase 0
7.     next(); // Wait for T2 to complete shared work in phase 0
8.     C(0); doWork(1);
9.   });
10.  asyncPhased(ph.inMode(SIG_WAIT), () -> { // Task T2
11.    A(1); doWork(1);    // Shared work in phase 0
12.    next(); // Wait for T1 to complete shared work in phase 0
13.    C(1); doWork(1);
14.    D(1); doWork(100); // Local work in phase 0
15.  });
16.}); // finish
```



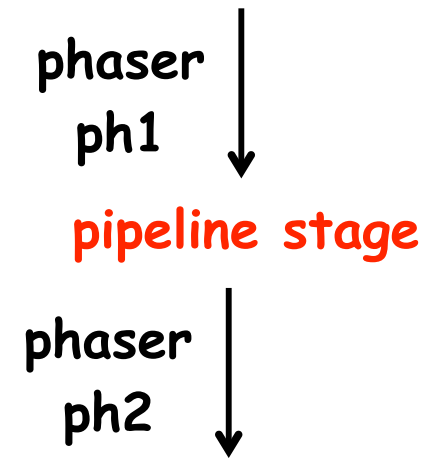
Recap of Producer-Consumer pattern with phasers (used for implementing pipeline parallelism)

```
1. asyncPhased(ph.inMode(SIG), () -> {
2.     for (int i = 0; i < rounds; i++) {
3.         buffer.insert(...);
4.         // producer can go ahead as they are in SIG mode
5.         next();
6.     }
7. });
8.
9. asyncPhased(ph.inMode(WAIT), () -> {
10.    for (int i = 0; i < rounds; i++) {
11.        next();
12.        buffer.remove(...);
13.    }
14. });
```



How to implement a pipeline stage that waits on one phaser and signals another?

```
1. asyncPhased(ph1.inMode(WAIT),
2.             ph2.inMode(SIG), () -> {
3.   for (int i = 0; i < rounds; i++) {
4.     // If we add next() here to wait on
5.     // ph1, it will also signal ph2.
6.     // Need an phaser-specific next!
7.     x = buffer1.remove();
8.     y = f(x);
9.     buffer2.insert();
10.  }
11. });
```

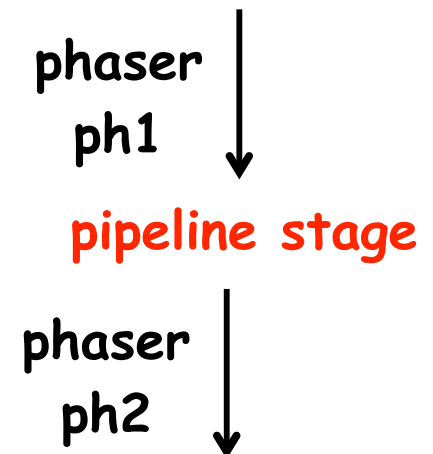


The diagram illustrates the flow of control between two phasers and a pipeline stage. A vertical arrow points downwards from the text 'phaser ph1' to the text 'pipeline stage'. From the 'pipeline stage', another vertical arrow points downwards to the text 'phaser ph2'. This indicates that the pipeline stage acts as a bridge, receiving a signal from ph1 and then signaling ph2.



Implementing a pipeline stage with phaser-specific doNext() operations

```
1. asyncPhased(ph1.inMode(WAIT),
2.             ph2.inMode(SIG), () -> {
3.   for (int i = 0; i < rounds; i++) {
4.     // wait-only operation on ph1
5.     ph1.doNext();
6.     x = buffer1.remove();
7.     y = f(x);
8.     buffer2.insert();
9.     // signal-only operation on ph2
10.    ph2.doNext();
11.  }
12. });
```



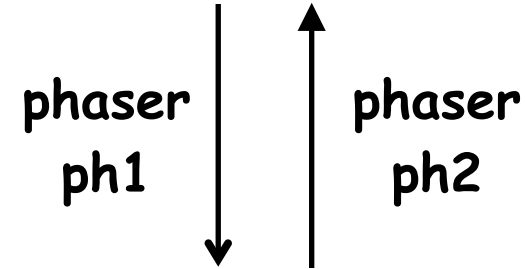
next() vs. phaser-specific doNext()

- **next()**
 - **General operation that first signals all phasers that current task is registered on in signal or signal-wait mode, and then waits on all phasers that current task is registered on in wait or signal-wait mode**
- **ph.doNext()**
 - **Performs a next operation restricted to a specific phaser**
 - **First signals ph, if current task is registered on ph in signal or signal-wait mode**
 - **Then waits on ph, if current task is registered on ph in wait or signal-wait mode**



Phaser-specific next operations can lead to deadlock, if used incorrectly

```
1. asyncPhased(ph1.inMode(SIG), ph2.inMode(WAIT), () -> {  
2.     for (int i = 0; i < rounds; i++) {  
3.         ph2.doNext(); // waits on ph2  
4.         . . .  
5.         ph1.doNext(); // signals ph1  
6.     }  
7. });
```



```
9. asyncPhased(ph2.inMode(SIG), ph1.inMode(WAIT), () -> {  
10.     for (int i = 0; i < rounds; i++) {  
11.         ph1.doNext(); // waits on ph1  
12.         . . .  
13.         ph2.doNext(); // signals ph2  
14.     }  
15. });
```



Summary of Parallel Programming Constructs you've learned so far

- Task Parallelism (Unit 1)
 - Async (task creation)
 - Finish (structured task termination)
- Functional Parallelism (Unit 2)
 - Future (task creation)
 - Future get() (task termination with return value)
 - Accumulators (functional reduction)
 - Map-Reduce (functional parallelism & reduction on key-value pairs)
- Loop Parallelism (Unit 3)
 - Forall (parallel loops)
 - Barriers (all-to-all synchronization)
- Dataflow Parallelism (Unit 4)
 - Data-Driven Tasks (dataflow parallelism)
 - Phasers (point-to-point synchronization)
 - Phaser-specific next operations



Semantic Property #1: Serializability

- Also referred to as “serial elision” property
 - A parallel program, P, satisfies the serial elision property if removing all parallel constructs results in a serial program, S, that represents a legal execution of program P
- Constructs that satisfy the serial elision property
 - Async (task creation)
 - Finish (structured task termination)
 - Future (task creation)
 - Future get() (task termination with return value)
 - Accumulators (functional reduction)
 - Map-Reduce (functional parallelism & reduction on key-value pairs)
 - Forall without barriers (parallel loops)



Example of a parallel program that satisfies the serial elision property

```
1. finish { // F1
2.   asyn A; // Boil pasta (20)
3.   finish { // F2
4.     asyn B1; // Chop veggies (5)
5.     asyn B2; // Brown meat (10)
6.   } // F2
7.   B3; // Make pasta sauce (10)
8. } // F1
```

Step B1



Step B2



Step B3



Step A



Example of a parallel program that does not satisfy the serial elision property

```
1. forallPhased (0, m - 1, (i) -> {  
2.   int sq = i*i;  
3.   System.out.println("Hello from task with square = " + sq);  
4.   next(); // Barrier  
5.   System.out.println("Goodbye from task with square = " + sq);  
6. });
```

Why does this program violate the serial elision property?



Semantic Property #2: Deadlock Freedom

- A parallel program, P , satisfies the deadlock freedom property if no execution of the program can reach a state in which one or more tasks are permanently blocked/suspended
- Constructs that satisfy the deadlock freedom property
 - Async (task creation)
 - Finish (structured task termination)
 - Future (task creation)
 - Future get() (task termination with return value)
 - Accumulators (functional reduction)
 - Map-Reduce (functional parallelism & reduction on key-value pairs)
 - Forall (parallel loops)
 - Barriers (all-to-all synchronization)
 - Phasers without phaser-specific next operations



Example of a parallel program that does not satisfy the deadlock-freedom property

```
1. HjDataDrivenFuture left = newDataDrivenFuture();
2. HjDataDrivenFuture right = newDataDrivenFuture();
3. finish(() -> {
4.     asyncAwait(left, () -> {
5.         right.put(rightWriter()); });
6.     asyncAwait(right, () -> {
7.         left.put(leftWriter()); });
8. });
```

Why does this program violate the deadlock-freedom property?



Semantic Property #3: Data Race Freedom

- A parallel program, P , satisfies the data race freedom property if no execution of the program can exhibit a data race
- In general, can only be guaranteed in very special cases e.g.,
 - Shared data that is allocated in futures or data-driven futures
 - Shared data this is immutable e.g., like Java strings
 - Shared data for which all steps that read or write it are totally ordered in the computation graph (includes case of “ownership” transfer from one task to another)



Example of a Data Race

```
1. // Start of Task T0 (main program)
2. sum1 = 0; sum2 = 0; // sum1 & sum2 are static fields
3. async { // Task T0 computes sum of lower half of array
4.     for(int i=0; i < X.length/2; i++)
5.         sum1 += X[i];
6. }
7. async { // Task T1 computes sum of upper half of array
8.     for(int i=X.length/2; i < X.length; i++)
9.         sum2 += X[i];
10. }
11. // Task T0 waits for Task T1 (join)
12. return sum1 + sum2;
```

Data race between accesses of sum1 in lines 5 and 12, and accesses of sum2 in lines 9 and 12



Semantic Property #4: Functional and Structural Determinism

- A **parallel program** is said to be *functionally deterministic* if it always computes the same answer when given the same input
- A **parallel program** is said to be *structurally deterministic* if it always produces the same computation graph when given the same input
- In general, functional and structural determinism can only be guaranteed in very special cases, because of potential for data races



Example with Functional Determinism and Structural Nondeterminism

```
1. static boolean found = false; //static
   field
2. . . .
3. finish for (int i = 0; i <= N - M; i++) {
4.     if (found) break; // Eureka!
5.     async {
6.         for (j = 0; j < M; j++)
7.             if (text[i+j] != pattern[j]) break;
8.         if (j == M) found = true;
9.     } // async
10. } // finish-for
```



Example with Structural Determinism and Functional Nondeterminism

// Index of an occurrence

```
1. static int index = -1; // static field
2. . . .
3. finish
4. for (int i = 0; i <= N - M; i++)
5.     async {
6.         for (j = 0; j < M; j++)
7.             if (text[i+j] != pattern[j]) break;
8.         if (j == M) index = i; // found at i
9.     }
```



Semantic Property #5: Data-Race-Free Determinism

- If a parallel program is known to be data-race-free, then it must be both functionally deterministic and structurally deterministic
- All HJlib constructs that you have learned thus far satisfy this property!
 - Does not apply to parallel Java constructs in general
 - Task Parallelism (Unit 1)
 - Async (task creation)
 - Finish (structured task termination)
 - Functional Parallelism (Unit 2)
 - Future (task creation)
 - Future get() (task termination with return value)
 - Accumulators (functional reduction)
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 - Loop Parallelism (Unit 3)
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 - Phaser-specific next operations



Semantic Properties of Parallel Programs

- ***Serializable* programs =**
{ async, finish, future }
- ***Deadlock-free* programs =**
***Serializable* U { barriers, phasers }**
- **Programs for which data-race-freedom implies both structural and functional determinism =**
***Deadlock-free* U { per-phaser next, async await }**

